

## **DIFFERENCES BETWEEN CEIBO XA EMULATORS**

EB-XA is the system with less features than DS-XA or EB-XAV2. EB-XA does not have real-time trace memory and memory is limited to 64K for emulation.

EB-XAV2 and DS-XA have longer memory capabilities and programmable clock generator, as well as real time trace.

\* Real-Time Trace: this is a real-time function and records program counter changes due to non-sequential instructions (call, jump, interrupt, etc.). The software completes the executed instructions between non-sequential instructions, so the trace depth is variable and according to this definition. This new concept allows a high performance usage of the trace memory. EB-XAV2 records 1024 non-sequential instructions. DS-XA records 8192 non-sequential instructions. The recreated trace information in unlimited.

FEATURES	EB-XA	EB-XAV2	DS-XA
Frequency of Operation	30 MHz	30 MHz	30 MHz
Emulation Memory	64K	512K	2M
Mapping Resolution	4K, 8K, 16K, 32K and 64K Blocks	32K Blocks	2K Blocks
Breakpoints	64K	512K	2M
Break on High Level Lines	Yes	Yes	Yes
Source Level Debugging for C and Assembler	Yes	Yes	Yes
Trace	No	Variable *	Variable *
Supported devices	G3, G49, S3	G3, G49, S3 and C3	G3, G49, S3 and C3
Leds and Switches for Experiments	Yes	Yes	No