

CEIBO

Development Tools of Choice



NEC Microcontrollers supported by Ceibo

DS-186 - In-Circuit Emulator

μ C	Probe	Header	Frequency	Voltage	Limitations	Special Features
V20 (PD70108)	A-86	40-DIP	20MHz	5V		Paradigm Software
V25 (PD70320/322)	A-V25	84-PLCC	20MHz	5V	no trace	Paradigm Software
V30 (PD70116)	A-86	40-DIP	20MHz	5V		Paradigm Software
V40 (PD70208)	A-V50	68-PLCC	20MHz	5V		Paradigm Software
V50 (PD70216)	A-V50	68-PLCC	20MHz	5V		Paradigm Software

Nomenclature:

Probe: means the emulation module that carries on the particular microprocessor

Header: is the mechanical attachment to be plugged into a target socket instead of actual chip

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